## Data sheet





General information	
Product type designation	CPU 1212C DC/DC/relay
Supply voltage	
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Load voltage L+	
Rated value (DC)	24 V
<ul> <li>permissible range, lower limit (DC)</li> </ul>	5 V
• permissible range, upper limit (DC)	250 V
Input current	
Current consumption (rated value)	400 mA; Typical
Current consumption, max.	1 200 mA; CPU with all expansion modules
Inrush current, max.	12 A; at 28.8 V
Output current	
for backplane bus (5 V DC), max.	1 000 mA; Max. 5 V DC for SM and CM

24 V encoder supply  24 V   L+ minus 4 V DC min.  Power loss Power loss, typ. 9 W  Memory  Memory  Integrated 75 kbyte expandable No Load memory  Integrated 1 Mbyte integrated 2	Encoder supply	
Power loss Power loss, typ. 9 W  Memory  Work memory  integrated 75 kbyte expandable No  load memory  integrated 1 Mbyte ellip-in (SIMATIC Memory Card), max. With SIMATIC memory card  Backup e present Yes; maintenance-free ellip-in (simath content of the state of t		
Power loss, typ. 9 W  Memory  Work memory  • integrated		L+ minus 4 V DC min.
Power loss, typ. 9 W  Memory  Work memory  • integrated	Power loss	
Work memory  integrated expandable No  land amemory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present without battery  CPU processing times  for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Besides  Number, max.  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Local data process image Inputs, adjustable I kbyte I hyte I hyte I kbyte		9 W
integrated expandable Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present without battery  in bit operations, typ. for bit operations, typ. for Blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Backup  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  4 kbyte; Size of bit memory address area  Local data per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image Inputs, adjustable I kbyte Plardware configuration		
integrated expandable  No  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present without battery  Present without battery  Present For bit operations, typ.  for word operations, typ.  for word operations, typ.  If yis, instruction  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Pata areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  Limited only by RAM for code  4 kbyte; Size of bit memory address area  Local data per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image I puputs, adjustable I kbyte  Later Ardware configuration		
expandable Load memory      integrated     Plug-in (SIMATIC Memory Card), max.  Backup     epresent     vithout battery     Yes; maintenance-free     vithout battery  CPU processing times  for bit operations, typ.     1.7 µs; / instruction  for word operations, typ.     1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  B Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag     Number, max.  Local data     eper priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I hputs, adjustable     1 kbyte  Later and the process image  I how the priority class adjustable     1 kbyte  Later and the process image  I kbyte  Later and the process image  Later and the process image and the process image  Later and the process image		75.11
Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present vithout battery  CPU processing times  for bit operations, typ. for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  Local data per priority class, max.  16 kbyte; Size of bit memory address area  Local starea  Process image Inputs, adjustable Ikbyte Outputs, adjustable Ikbyte  Limited only by RAM for code  1 kbyte 1 kbyte  Limited only class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image Inputs, adjustable Ikbyte Outputs, adjustable Ikbyte  Limited only the max in the memory address area  Local data  Ikbyte  Limited only by RAM for code		
Integrated Plug-in (SIMATIC Memory Card), max.  Backup  present without battery  Press  CPU processing times  For bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  Nutyper Ardware configuration	·	No
Plug-in (SIMATIC Memory Card), max.  Backup  present present without battery  Yes; maintenance-free Yes  CPU processing times  for bit operations, typ. 0.085 µs; / instruction  for word operations, typ. 1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  Local data per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image Inputs, adjustable Inputs, adjustable Outputs, adjustable Utype Age Size of bit memory address area I kbyte I kbyte  Hardware configuration		
Backup  present without battery  Pes: maintenance-free without battery  Pes  CPU processing times  for bit operations, typ. 1.7 µs; / instruction  for floating point arithmetic, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  I kbyte  Outputs, adjustable  1 kbyte  Hardware configuration	• integrated	
Present     without battery     Yes  CPU processing times  for bit operations, typ.     for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Base Possible blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Relentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I kbyte  Outputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration	Plug-in (SIMATIC Memory Card), max.	with SIMATIC memory card
Processing times  for bit operations, typ.  for floating point arithmetic, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Bata areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Limited only by RAM for code  10 kbyte  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I hputs, adjustable  I kbyte  Outputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration	Backup	
CPU processing times  for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I hputs, adjustable  I kbyte  Outputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte	• present	Yes; maintenance-free
for bit operations, typ.  for word operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Local data  Per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I lkbyte  Outputs, adjustable  1 kbyte  1 kbyte  Hardware configuration	<ul><li>without battery</li></ul>	Yes
for bit operations, typ.  for word operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Local data  Per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I lkbyte  Outputs, adjustable  1 kbyte  1 kbyte  Hardware configuration	CPU processing times	
for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I kbyte  Outputs, adjustable  Number, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration		0.085 μs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  Outputs, adjustable  Number, max.  1 kbyte  1 kbyte  1 kbyte	for word operations, typ.	1.7 µs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I kbyte  Outputs, adjustable  1 kbyte  Hardware configuration	for floating point arithmetic, typ.	2.3 µs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  I kbyte  Outputs, adjustable  1 kbyte  Hardware configuration	CPU-blocks	
restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration		DBs, FCs, FBs, counters and timers. The maximum number of
OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  I kbyte  Outputs, adjustable  1 kbyte  Hardware configuration		addressable blocks ranges from 1 to 65535. There is no
<ul> <li>Number, max.</li> <li>Limited only by RAM for code</li> </ul> Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. <ul> <li>Number, max.</li> <li>Number, max.</li> <li>A kbyte; Size of bit memory address area</li> </ul> Local data <ul> <li>per priority class, max.</li> <li>16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB</li> </ul> Address area Process image <ul> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>1 kbyte</li> </ul> Hardware configuration Hardware configuration		restriction, the entire working memory can be used
Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  • Inputs, adjustable  • Outputs, adjustable  • Outputs, adjustable  1 kbyte  Hardware configuration	ОВ	
Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  4 kbyte; Size of bit memory address area  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  • Inputs, adjustable  • Outputs, adjustable  • Outputs, adjustable  Hardware configuration	• Number, max.	Limited only by RAM for code
Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  Outputs, adjustable  1 kbyte  Hardware configuration	Data areas and their retentivity	
Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  Inputs, adjustable  Outputs, adjustable  1 kbyte  Hardware configuration	Retentive data area (incl. timers, counters, flags),	10 kbyte
● Number, max.  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image  ● Inputs, adjustable  ● Outputs, adjustable  ● Outputs, adjustable  Hardware configuration	max.	
Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB   Address area  Process image  • Inputs, adjustable  • Outputs, adjustable  • Outputs, adjustable  1 kbyte  Hardware configuration	Flag	
<ul> <li>per priority class, max.</li> <li>16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB</li> <li>Address area</li> <li>Process image         <ul> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>I kbyte</li> </ul> </li> <li>Hardware configuration</li> </ul>	Number, max.	4 kbyte; Size of bit memory address area
Address area  Process image  Inputs, adjustable  Outputs, adjustable  Hardware configuration	Local data	
Process image  Inputs, adjustable Outputs, adjustable  1 kbyte  1 kbyte  Hardware configuration	<ul> <li>per priority class, max.</li> </ul>	
<ul> <li>Inputs, adjustable</li> <li>Outputs, adjustable</li> <li>Hardware configuration</li> </ul>	Address area	
Outputs, adjustable     1 kbyte  Hardware configuration	Process image	
Hardware configuration	Inputs, adjustable	1 kbyte
	Outputs, adjustable	1 kbyte
	Hardware configuration	
	<u> </u>	3 comm. modules, 1 signal board, 2 signal modules

Time of day	
Clock	
Hardware clock (real-time)	Yes
Backup time	480 h; Typical
<ul><li>Deviation per day, max.</li></ul>	60 s/month at 25 °C
Digital inputs	
Number of digital inputs	8; Integrated
<ul> <li>of which inputs usable for technological functions</li> </ul>	4; HSC (High Speed Counting)
Source/sink input	Yes
Number of simultaneously controllable inputs	
all mounting positions	
— up to 40 $^{\circ}$ C, max.	8
Input voltage	
Rated value (DC)	24 V
• for signal "0"	5 V DC at 1 mA
• for signal "1"	15 V DC at 2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes
for technological functions	
— parameterizable	Single phase: 3 @ 100 kHz & 1 @ 30 kHz, differential: 3 @ 80 kHz & 1 @ 30 kHz
Cable length	
• shielded, max.	500 m; 50 m for technological functions
• unshielded, max.	300 m; For technological functions: No
Digital outputs	
Number of digital outputs	6; Relays
Switching capacity of the outputs	
<ul><li>with resistive load, max.</li></ul>	2 A
● on lamp load, max.	30 W with DC, 200 W with AC
Output delay with resistive load	
• "0" to "1", max.	10 ms; max.
● "1" to "0", max.	10 ms; max.
0 11111	
Switching frequency	
of the pulse outputs, with resistive load, max.	1 Hz

Number of relevanteurs	6
Number of relay outputs	
Number of operating cycles, max.	mechanically 10 million, at rated load voltage 100 000
Cable length	F00
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	
Number of analog inputs	2
Input ranges	
<ul><li>Voltage</li></ul>	Yes
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
<ul><li>Input resistance (0 to 10 V)</li></ul>	≥100k ohms
Cable length	
• shielded, max.	100 m; twisted and shielded
Analog outputs	
Number of analog outputs	0
Analog value generation for the inputs	
Integration and conversion time/resolution per channel	40 h.i.
<ul> <li>Resolution with overrange (bit including sign), max.</li> </ul>	10 bit
	Yes
Integration time, parameterizable	
Conversion time (per channel)	625 μs
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
1. Interface	
Interface type	PROFINET
Physics	Ethernet
Isolated	Yes
automatic detection of transmission rate	Yes
Autonegotiation	Yes
Autocrossing	Yes
Protocols	
PROFINET IO Controller	Yes
PROFINET IO Device	Yes
Open IE communication	Yes
Web server	Yes
PROFINET IO Controller	
Transmission rate, max.	100 Mbit/s
Services	

<ul> <li>Number of connectable IO Devices, max.</li> </ul>	16
PROFINET IO Device	
Services	
— Shared device	Yes
<ul> <li>Number of IO Controllers with shared</li> </ul>	2
device, max.	
Protocols	
Supports protocol for PROFINET IO	Yes
PROFIBUS	Yes; CM 1243-5 required
AS-Interface	Yes
Protocols (Ethernet)	1.00
• TCP/IP	Yes
Open IE communication	
• TCP/IP	Yes
• ISO-on-TCP (RFC1006)	Yes
• UDP	Yes
Web server	163
	Yes
• supported	Yes
User-defined websites  Further protection	165
Further protocols	Yes
• MODBUS	Tes
Communication functions	
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes
Number of connections	
Number of connections  • overall	16; dynamically
• overall	16; dynamically
overall  Test commissioning functions	16; dynamically
overall  Test commissioning functions  Status/control	
overall  Test commissioning functions     Status/control      Status/control variable	Yes
overall  Test commissioning functions  Status/control	
overall  Test commissioning functions Status/control      Status/control variable     Variables	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers,
overall  Test commissioning functions     Status/control      Status/control variable     Variables  Forcing	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers,
overall  Test commissioning functions  Status/control      Status/control variable     Variables  Forcing     Forcing	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
overall  Test commissioning functions     Status/control          • Status/control variable         • Variables  Forcing         • Forcing         Diagnostic buffer	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
overall  Test commissioning functions  Status/control      Status/control variable      Variables  Forcing     Forcing  Diagnostic buffer      present	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters  Yes
overall  Test commissioning functions  Status/control      Status/control variable      Variables  Forcing     Forcing  Diagnostic buffer      present  Traces	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters  Yes
overall  Test commissioning functions  Status/control      Status/control variable     Variables  Forcing     Forcing  Diagnostic buffer     present  Traces  Number of configurable Traces	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters  Yes  Yes
overall  Test commissioning functions  Status/control      Status/control variable      Variables  Forcing     Forcing  Diagnostic buffer      present  Traces	Yes Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters  Yes  Yes

Counting frequency (counter) max.	100 kHz
Frequency measurement	Yes
controlled positioning	Yes
Number of position-controlled positioning axes, max.	8
Number of positioning axes via pulse-direction interface	Up to 4 with SB 1222
PID controller	Yes
Number of alarm inputs	4
Potential separation	
Potential separation digital inputs	
Potential separation digital inputs	500V AC for 1 minute
• between the channels, in groups of	1
Potential separation digital outputs	
Potential separation digital outputs	Relays
• between the channels	No
• between the channels, in groups of	2
EMC	
Interference immunity against discharge of static electr	icity
<ul> <li>Interference immunity against discharge of static electricity acc. to IEC 61000-4-2</li> </ul>	Yes
Test voltage at air discharge	8 kV
Test voltage at contact discharge	6 kV
Interference immunity to cable-borne interference	
Interference immunity on supply lines acc. to	Yes
IEC 61000-4-4	
<ul> <li>Interference immunity on signal cables acc. to IEC 61000-4-4</li> </ul>	Yes
Interference immunity against voltage surge	
• on the supply lines acc. to IEC 61000-4-5	Yes
Interference immunity against conducted variable distu	rbance induced by high-frequency fields
<ul> <li>Interference immunity against high-frequency radiation acc. to IEC 61000-4-6</li> </ul>	Yes
Emission of radio interference acc. to EN 55 011	
• Limit class A, for use in industrial areas	Yes; Group 1
• Limit class B, for use in residential areas	Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011

## Ambient conditions

• IP20

Degree and class of protection

Degree of protection acc. to EN 60529

Free fall

• Fall height, max. 0.3 m; five times, in product package

Yes

Ambient temperature during operation	
• min.	-20 °C; = Tmin; Startup @ 0 °C
• max.	60 °C; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at 60 °C horizontal or 50 °C vertical, 8 or 6 at 55 °C horizontal or 45 °C vertical
<ul> <li>horizontal installation, min.</li> </ul>	-20 °C
<ul> <li>horizontal installation, max.</li> </ul>	60 °C
• vertical installation, min.	-20 °C
<ul> <li>vertical installation, max.</li> </ul>	50 °C
Ambient temperature during storage/transportation	
• min.	-40 °C
• max.	70 °C
Altitude during operation relating to sea level	
<ul> <li>Installation altitude above sea level, max.</li> </ul>	2 000 m
<ul> <li>Ambient air temperature-barometric pressure- altitude</li> </ul>	Tmin Tmax at 1 140 hPa 795 hPa (-1 000 m +2 000 m) // Tmin (Tmax - 10 K) at 795 hPa 658 hPa (+2 000 m +3 500 m) // Tmin (Tmax - 20 K) at 658 hPa 540 hPa (+3 500 m +5 000 m); above 2 000 m max. 132 V AC
Relative humidity	
<ul> <li>With condensation, tested in accordance with IEC 60068-2-38, max.</li> </ul>	100 %; RH incl. condensation/frost (no commissioning under condensation conditions)
Vibrations	
<ul> <li>Vibration resistance during operation acc. to IEC 60068-2-6</li> </ul>	2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail
<ul> <li>Operation, tested according to IEC 60068-2-6</li> </ul>	Yes
Shock testing	
• tested according to IEC 60068-2-27	Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
Resistance	
Coolants and lubricants	
<ul> <li>Resistant to commercially available coolants and lubricants</li> </ul>	Yes
Use in stationary industrial systems	
<ul> <li>to biologically active substances according to EN 60721-3-3</li> </ul>	Yes; Class 3B2 mold, fungus and dry rot spores (with the exception of fauna); Class 3B3 on request
<ul> <li>to chemically active substances according to EN 60721-3-3</li> </ul>	Yes; Class 3C4 (RH < 75 %) incl. salt spray acc. to EN 60068-2-52 (severity degree 3); $^{\star}$
<ul> <li>to mechanically active substances according to EN 60721-3-3</li> </ul>	Yes; Class 3S4 incl. sand, dust, *
Use on ships/at sea	
<ul> <li>to biologically active substances according to EN 60721-3-6</li> </ul>	Yes; Class 6B2 mold and fungal spores (excluding fauna); Class 6B3 on request
<ul> <li>to chemically active substances according to EN 60721-3-6</li> </ul>	Yes; Class 6C3 (RH < 75 %) incl. salt spray acc. to EN 60068-2-52 (severity degree 3); $^{\star}$

<ul> <li>to mechanically active substances</li> </ul>	Yes; Class 6S3 incl. sand, dust; *
according to EN 60721-3-6	
Remark	
Note regarding classification of	* The supplied plug covers must remain in place over the unused
environmental conditions acc. to EN 60721	interfaces during operation!
Configuration	,
Programming	
Programming language	
— LAD	Yes
— FBD	Yes
— SCL	Yes
Cycle time monitoring	
adjustable	Yes
Dimensions	
Width	90 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	385 g

08/01/2018

last modified: